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10/072,145

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Averett, Guy E. et al.
Serial No.: 10/072,145
Filing Date: 02/07/2002
Group Art Unit: 2811
Examiner: Ori Nadav
For: **SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF
LOW SUBSTRATE CAPACITANCE**

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facsimile transmitted to the United States Patent and
Trademark Office, attn: Examiner Ori Nadav

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Lydia McNamara (602.244.5306)

12 pages

AMENDMENT

Honorable Commissioner of Patents,
P.O. BOX 1450
Alexandria, VA 22313-1450

SIR:

In response to the office action mailed August 25,
2005, please reconsider the above-identified application in
view of the amendments and remarks presented hereinafter.